Certificate

of Correction

PATENT

dereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Brent Keeth, Brian M. Shirley,

Attorney Docket No.: 500644.03

Charles H. Dennison and

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Patent No. : US 6,948,027 B2

Issued : September 20, 2005

Title : METHOD AND SYSTEM FOR USING DYNAMIC RANDOM ACCESS MEMORY AS

CACHE MEMORY

NOTIFICATION OF ERRORS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

SEP: 1-8 2007

The following errors were noted in a review of the above-identified letters patent. One or more of these errors was inadvertently made in the original application, while the others occurred in the printing of the patent. Since the errors are of an obvious nature, a formal Certificate of Correction is not believed to be warranted at this time. Therefore, applicants request that this notification be placed in the Patent and Trademark Office file.

Column, Line	Reads	Should Read
Column 1, Line 13	"directed memory devices,"	directed to memory devices,
Column 2, Line 37	"FIG. 3, is a diagram"	FIG. 3 is a diagram
Column 2, Lines 39-40	"pair of complimentary"	pair of complementary
Column 3, Lines 61-	"refreshed may be as	refreshed may also be used as
62	auxiliary"	auxiliary

Column 4, Line 10	"normal number of input"	normal number of complementary input
Column 4, Line 11	"lines 62 pairs,"	line pairs 62 ,
Column 4, Line 13	"of complimentary I/O"	of complementary I/O
Column 4, Line 46	"from that that portion."	from that portion
Column 4, Line 61	"from first one"	from the first one
Column 5, Line 3	"for to wait for"	to wait for
Column 6, Lines 2 and 34	"complimentary"	complementary
Column 7, Line 39	"complimentary"	complementary
Column 8, Line 13	"complimentary"	complementary
Column 8, Line 36	"and wherein in the"	and wherein the
Column 9, Line 4	"further comprising"	further comprising:
Column 9, Line 17	"memory each of which"	memory, each of which
Column 10, Line 23	"is directed and"	is directed; and

Respectfully submitted,

Date: Sept. 11, 2007

By: _

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